




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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

10/069510

APPL NUM 10069510	FILING DATE 06/20/2002	CLASS 219	SUBCLASS 444.100	GAU 3742	EXAMINER Rik Fagua
**APPLICANTS: Hiramatsu Yasuji; Ito Yasutaka;					
**CONTINUING DATA VERIFIED: This application is a 371 of PCT/JP01/05791 07/04/2001 					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-202510 07/04/2000 					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials 		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 220254US2PCT	
TITLE : Hot plate for semiconductor manufacture and testing					
U.S. DEPT. OF COMM. / PAT & TM-PTO 436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims: _____	
		Print Claim for O.G. _____	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
			Print Fig.
<input type="checkbox"/> TERMINAL		Primary Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
		Application Examiner	
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